



UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE
United States Patent and Trademark Office
Address: COMMISSIONER FOR PATENTS
P.O. Box 1450
Alexandria, Virginia 22313-1450
www.uspto.gov

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/632,751	08/01/2003	Mark S. Myers	P2002,0527	4657

24131 7590 06/29/2005

LERNER AND GREENBERG, PA
P O BOX 2480
HOLLYWOOD, FL 33022-2480

EXAMINER

JEAN, FRANTZ B

ART UNIT	PAPER NUMBER
----------	--------------

2151

DATE MAILED: 06/29/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary

Application No.

10/632,751

Applicant(s)

MYERS, MARK S.

Examiner

Frantz B. Jean

Art Unit

2151

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --
Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 01 August 2003.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-7 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 1-7 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on _____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. _____.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
- * See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- 1) ☒ Notice of References Cited (PTO-892)
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3) ☒ Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date 8/01/03.
- 4) ☐ Interview Summary (PTO-413)
Paper No(s)/Mail Date. _____.
- 5) ☐ Notice of Informal Patent Application (PTO-152)
- 6) ☐ Other: _____.

DETAILED ACTION

This is a first office action in response to application filed on 08/01/03. Claims 1-7 are presented for examination.

Information Disclosure Statement

The information disclosure statement (IDS) submitted on 08/01/2003 is in compliance with the provisions of 37 CFR 1.97. Accordingly, the information disclosure statement is being considered by the examiner.

Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

Claims 1-7 are rejected under 35 U.S.C. 102(b) as being anticipated by Passint et al. hereinafter "Passint" US patent Number 5,581,705.

As per claim 1, Passint teaches in a parallel processing system (MPP) col. 2 lines 14-26), a method of sending a message across an interconnected network (see fig 1, element 106) from a first compute node (101), which has a processor (102) and a memory (104), to a second compute node, which has a processor and a memory (see fig 1), the method which comprises: in the first compute node, setting a message header field of the message to a predetermined value and sending the message (see abstract, col. 2 line 26 to col. 3 line 5; col 5 line 61 to col. 6 line 37); in the second node,

Art Unit: 2151

receiving and processing the message header, and reading a memory location in accordance with contents of a base address register and an index register (fig 11; col. 8 line 45 to col. 10 line 55; col. 12 lines 55 et seq); and using DMA, storing the message in a memory at a storage address determined in accordance with contents of the memory location (see fig 2).

As per claim 2, Passint teaches a storage address that is aligned on a memory page boundary (see fig 2).

As per claim 3, Passint teaches incrementing the index register (see fig 6; col. 2 lines 57 et seq).

As per claim 4, Passint teaches decrementing a size register indicative of a number of storage addresses stored in a portion of memory (see col 9 et seq).

As per claims 5-7, Passint teaches checking the contents of the memory location (col. 2 lines 54 et seq), comparing a field of the contents of the memory location with the message header field (see fig 10-11; col. 7 lines 42 et seq), and providing the contents of the memory location with a valid bit ... (see col. 6 lines 5 et seq).


Any inquiry concerning this communication or earlier communications from the examiner should be directed to Frantz B. Jean whose telephone number is 571-272-3937. The examiner can normally be reached on 8:30-6:00 M-f.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Zarni Maung can be reached on 571 272 3939. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Art Unit: 2151

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Frantz Jean



FRANTZ B. JEAN
PRIMARY EXAMINER